sயリтとHEs Application Notes for LCD 36x24 SmartDisplay

# Application Notes for the LCD 36x24 SmartDisplay 

Revision D

## SMARTDISPLAY ${ }^{w}$



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## 

## 1.General Information

The application notes should be used in conjunction with the LCD $36 \times 24$ data sheet which has the LED, LCD, and other specifications as well as the timing diagram for the communication.

## 2.Part Numbers

## Switches

The LCD 36x24 SmartDisplay is currently available as $36 \times 24$ standard pushbuttons, $36 \times 24$ compact pushbuttons, and $36 \times 24$ displays. The pushbuttons have an RBG or bi-color option for backlighting. For the purpose of these application notes, all products are referred to as modules. For prototyping, it is recommended to use the relevant SmartDisplay socket accessory.

| Switch | PN | Socket | Description |
| :---: | :---: | :---: | :---: |
| LCD $36 \times 24$ RGB <br> Pushbutton | IS15BBFP4RGB | AT9704-065E | Standard size, RGB <br> backlight |
| Compact LCD $36 \times 24$ RGB <br> Pushbutton | IS15BSBFP4RGB | AT9704-065F | Compact size, RBG <br> backlight |
| $36 \times 24$ RGB Display | IS01BBFRGB | Industry standard <br> 2 mm $\times 2 m m$ (example: <br> $79107-7005)$ | Display only |
| LCD $36 \times 24$ Bicolor <br> Pushbutton | IS15BAFP4CF | AT9704-02YC <br> *AT9704-065E | Standard size, bi-color <br> backlight |
| Compact LCD $36 \times 24$ Bicolor <br> Pushbutton | IS15BSAFP4CF | AT9704-065F | Compact size, bi-color <br> backlight |

*AT9704-065E can be used with IS15BAFP4CF by removing pin 3.

## Engineering Kits

Many variations of Engineering Kits can be made simply by soldering on various sockets (table below). Only the Engineering Kit options marked in green are marketed and kept in stock. However, all of them can be ordered.

All the Engineering Kits come with switches/display on the sockets and a USB cable. Controlling all the LCD $36 \times 24$ kits is the same.

| Part Number | Switch 1 / Display 1 |  | Switch 2 |  |
| :---: | :---: | :---: | :---: | :---: |
| IS-ENG-KIT-5-DC | $36 \times 24$ RGB Display | IS01BBFRGB | Compact LCD $36 x$ 24 RGB Pushbutton | IS15BSBFP4RGB |
| IS-ENG-KIT-5-SC | $\text { LCD } 36 \times 24 \text { RGB }$ <br> Pushbutton | IS15BBFP4RGB | Compact LCD 36 x 24 RGB Pushbutton | IS15BSBFP4RGB |
| IS-ENG-KIT-5-BC | LCD $36 \times 24$ Bicolor Pushbutton | IS15BAFP4CF | Compact LCD 36 x 24 RGB Pushbutton | IS15BSBFP4RGB |
| IS-ENG-KIT-5-SS | $\text { LCD } 36 \times 24 \text { RGB }$ Pushbutton | IS15BBFP4RGB | LCD $36 \times 24$ RGB Pushbutton | IS15BBFP4RGB |
| IS-ENG-KIT-5-CC | Compact LCD 36 x 24 RGB Pushbutton | IS15BSBFP4RGB | Compact LCD 36 x 24 RGB Pushbutton | IS15BSBFP4RGB |
| IS-ENG-KIT-5-BB | LCD $36 \times 24$ Bicolor Pushbutton | IS15BAFP4CF | LCD $36 \times 24$ Bicolor Pushbutton | IS15BAFP4CF |



## IS－C3201 Controller

The IS－C3201 Intelligent Controller controls up to 32 LCD $36 \times 24$ switches／displays．The IS－C3201 is designed to be used in many different applications．There is total flexibility with user－defined features that allow the controllers to be programmed for specific applications．The user defined data and set－up are stored in a non－volatile memory and specify the way the system behaves．The firmware can be customized based on customer requirements．Logic boards are required．

Logic boards are switch panels that have glue logic to convert addressing and switch scanning to serial． A logic board can be designed for any number of switches．They can be daisy－chained using a 14－pin ribbon cable allowing for a variable number of switches to be controlled from one port of a controller． See the LCD 36x24 Logic Boards documentation on our website for more information．

## General features：

－Control up to 32 LCD 36x24 switches／displays．Two banks of 16.
－USB，RS232，RS422／RS485 communication（115，200 baud）．
－User downloadable images and backlight colors．
－Look up tables for fonts $5 \times 7$ and $7 \times 10$ ．
－ 8 brightness settings．
－Memory for 16，000 images，backlights，and attributes．
－Reports switch activities via serial port．
－Stand－alone operation or real time control by host．
－A 10 pins Auxiliary port with 7 MC pins for control or sense other devices．
－User defined blinking durations．
－Firmware upgradable via USB．
Switch－action report from the controller to the host：
－On switch press／release status change，the new statuses are sent over the last communication interface．

## Real－time operation features：

－Download graphic image data to any switch．
－Select any image from flash memory to display on any of switches．
－Create $6 \times 8$ font string for a specified row on a specified switch．
－Create $9 x 12$ font string for a specified row on a specified switch．
－Change the backlight color on any switch．
See the IS－C3201 documentation on our website for more information．



## 3.Pin-outs

The standard size LCD $36 \times 24$ RGB pushbuttons have a different pin-out from the standard size bi-color pushbuttons. All the compact size pushbuttons have the same pin-out. All the displays have the same pin-out.

All the functions are the same regardless of the pin-outs, so for the purpose of these application notes, the RGB SmartDisplay will be used as the example. The following are the pin-outs for the RGB SmartDisplay.

LCD 36x24 RGB SmartDisplay Pinout:

| Pins | Symbol | Pin Name | Function |
| :--- | :--- | :--- | :--- |
| 1 | SW | Switch Terminal | Normally open switch |
| 2 | SW | Switch Terminal | Normally open switch |
| 3 | BL-LED(-) | LED Backlight Terminal | Cathode for Red |
| 4 | BL-LED(-) | LED Backlight Terminal | Cathode for Blue |
| 5 | Dout | Data Output | To daisy chain, connect to Din of next <br> switch. |
| 6 | FLM | First Line Marker | Control line for LCD display |
| 7 | LP | Latch Pulse | Control line for LCD display |
| 8 | SCP | Serial Clock Pulse | Clock for shifting data of LCD display |
| 9 | Din | Data Input | Data input for LCD display |
| 10 | GND | Ground |  |
| 11 | VDD | Power | +5 V power source for logic circuit |
| 12 | VLC | LCD Power | Power source for LCD drive |
| 13 | BL-LED(+) | LED Backlight Terminal | Common Anode for LEDs |
| 14 | BL-LED(-) | LED Backlight Terminal | Cathode for Green |

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## 4.Pin Descriptions and Functions

Switch terminals (SW, SW): The switch is normally open. The switch can be scanned by connecting one pinto Ground and the other pin to a micro-controller. For a matrix of switches many different methods can be used for scanning.

Ground: The Ground for logic, LCD, and LED.
VDD: Power source for logic ( 5 V and 1 mA ).
VLC: Supply voltage for LCD ( 7.3 V to 7.5 V and less than 1 mA ). The required voltage value depends on the refresh rate of the LCD display, the temperature, and the desired viewing angle. It is recommended that this voltage be adjustable. This can be achieved by using a potentiometer between higher voltage such as 9 V or 12 V and Ground or using an OpAmp.

BL-LED(+), BL-LED(-), BL-LED(-), BL-LED(-): LED backlighting as shown in the diagram below.


The LED forward voltages for the LCD $36 \times 24$ RGB SmartDisplay are as follows:
LED (red) = 2.1 V
LED (green) $=3.3 \mathrm{~V}$
LED (blue) $=3.3 \mathrm{~V}$
The absolute maximum current rating is 20 mA . A current limiting resistor should be used. The brightness of the backlighting is determined by the current value. Other colors are achieved by different combinations of red, green, and blue.

Note: Please refer to the data sheets for the specific switch of interest as the LED specifications will be different than the LCD $36 \times 24$ RGB SmartDisplay.

SCP and Din: Clock and Data communication. These are connected to data in and clock of an internal 40 -bit shift register of the SmartDisplay. The clock pin and the data pin can be connected to the pins of a micro-controller. The clock line should be normally high. First, the data bit is set on the data line and then the clock is toggled.

The LCD display has 36 pixels in each row. Positions 37, 38, 39 and 40 of the internal shift register are not used. The maximum clock frequency is rated for 6.0 MHz .

FLM and LP: Control lines for the internal LCD driver. These lines are connected to micro-controller pins. They are normally low. LP is toggled after the data is shifted to the right position in the internal shift register of the SmartDisplays. FLM is set to high before shifting the data for the first line of the display, and cleared after the data is shifted and LP is toggled for the first line.

Dout: Data out. This pin is the output of the 40th position of the internal shift register. It is used to daisy chain the SmartDisplays. It connects to data in (pin 8) of the next switch.

## 5.Controlling the SmartDisplay LED Backlighting

Backlighting can be controlled using current limiting resistors and a driver IC with latch as shown below:


The circuit below results in consistent brightness between single LED ON and multiple LEDs ON.


Note: It is recommended to use an LED driver such as the MAX6957 for controlling the backlighting. This eliminates the need for resistors and a driver IC with latch. It allows for achieving any color as well as color matching via software control.

## 6．Controlling Multiple Modules

a）All the SCP pins are connected to the same source．
b）All the LP pins are connected to the same source．
c）All the FLM pins are connected to the same source．
d）Din of the first switch is connected to the source．Dout of the first switch is connected to Din of second switch etc．．．

Note：A buffer IC should be used for SCP，LP，and FLM if the fan－out of the source is not sufficient． As the number of switches increases，directly controlling the clock and data by the micro－controller becomes inefficient．A different method should be used to shift the data faster．Some possible options are using a serial port in 8－bit mode or using three shift registers．Three parallel－in serial－out shift registers can be used to accomplish this task as shown in Figure 1．This circuit converts the parallel byte to serial．

Figure 1：Multiple Modules Controlling Example Using Three Shift Registers
FIG． 1


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## 7.Bit Map Picture Format for One Module

The display on the SmartDisplay is a graphic LCD with a matrix of 36 columns by 24 rows. The SmartDisplay has an internal 40-bit shift register. The first 36 bits of the shift register correspond to the 36 columns of the display and the last 4 bits of the shift register are not used. See Figure 2. The internal shift register shifts left to right, so the 40th bit is shifted first and the first bit is shifted last.

The first four bits of data shifted in are not used. The fifth bit corresponds to the right most pixel of the row and the 40th bit of data shifted in corresponds to the left most pixel of the row. A high bit causes the corresponding pixel to be on and a low bit causes the corresponding pixel to be off. When the switches are daisy-chained, the first forty bits of data shifted in correspond to the last switch, and the last forty bits of data shifted in correspond to the first switch.

LP and FLM are normally low. FLM must be set high before shifting the data for the first row, and cleared after the data is shifted and the LP is toggled. For rows 2 to 24 , after the data is shifted, each row LP must be toggled.

Figure 2: bit map on 40-bit shift register
$\begin{array}{llllllllllllllllllllllllllllll}1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 2 & 2 & 2 & 2 & 2 & 2 & 2 & 2 & 2 & 2 & 3 & 3 & 3 & 3 & 3 & 3 & 3 & 3 & 3 & 3\end{array}$ $\begin{array}{llllllllllllllllllllllllllllllllllllllll}1 & 2 & 3 & 4 & 5 & 6 & 7 & 8 & 9 & 0 & 1 & 2 & 3 & 4 & 5 & 6 & 7 & 8 & 9 & 0 & 1 & 2 & 3 & 4 & 5 & 6 & 7 & 8 & 9 & 0 & 1 & 2 & 3 & 4 & 5 & 6 & 7 & 8 & 9 & 0\end{array}$


The memory data specified by grey are not used.

## 8. Refresh and LP Timing

The display must be refreshed continuously. The LP-to-LP timing must be consistent. LP causes the internal LCD driver to use the data from the internal 40-bit shift register to energize the pixels of the corresponding row. If the LP-to-LP timing is not consistent, the pixel rows that are energized for a longer time will be darker. If a row gets charged for too long, it could damage the display.

When the LP-to-LP timing exceeds 1.2 ms , a flicker will be noticed on the displays. Lower LP-to-LP timing up to 0.7 ms causes better contrast. However, LP-to-LP timing below 0.7 ms does not cause significant contrast improvement.

## 9.Timer Interrupt

A timer interrupt should be used for refreshing the display, backlighting, and switch scan. The timer interrupt interval should be equal to the desired LP-to-LP timing. The timer interrupt should be set to low priority.

The following SmartDisplay related functions could be performed by timer interrupt routines:
a) Start of interrupt routine.
b) If it is the first line of the display, set the FLM to high.
c) Shift the data for the corresponding row of displays.
d) Toggle the LP.
e) Set the FLM low.
f) Put LED data into effect.
g) Increment the line number of the display. If equal to 25 , set it to 1 .
h) If the line number for the display is equal to 1, then scan the switches. This compensates for switch bouncing.
i) End of interrupt routine.

Manipulation of data and any other tasks can be done by the main program.

## 10. Sample Schematics

Figure 3: Sample schematic for controlling 2 RGB SmartDisplays.


Figure 4: Sample schematic for controlling 3 RGB SmartDisplays.


Continued on the next page.

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Figure 3: Sample schematic for controlling 3 RGB SmartDisplays (continued).


Please note the microcontroller can drive up to 48 SmartDisplays.

## 11. Frequently Asked Questions

## Does the display have to be refreshed?

Yes. The displays must be refreshed. The drawback for the switches that have a refreshing circuit in the switch cap, is that the controller cannot detect when the refreshing circuit freezes due to an ESD charge.

## Why is one of the display rows darker than the rest?

The timing between the latch pulse of that row and the next row is longer than the timing between the rest of latch pulses.

## What happens if the display is not refreshed?

If the VLC is present, one row of display gets charged for a long time, which can damage the display. If the micro-controller goes into sleep mode, it must disable the VLC.

## Does the micro-controller have to have external memory?

For many switches, having memory is useful for keeping a large number of pictures. For a small number of switches, the pictures can be made on the fly using ASCII code and a look up table without using external memory.

## Is the display visible without the backlighting?

Yes. The LCD in the switch is transflective so it can be seen with sufficient ambient lighting.

## Is the display sunlight readable?

Since the LCD is transflective, it is sunlight readable. However, there will be reflection from the lens.

## How many switches can be driven by a micro-controller?

The number of Modules that a micro-controller can control depends on the instruction execution time, other tasks performed, hardware design, and software design.

## Example:

Given: A micro-controller with an average instruction execution time of 1 ms and with $34 \%$ of the time performing other tasks. The LP-to-LP timing is 1 ms .
Calculate: The number of modules that can be controlled by the micro-controller.
Solution: Time to refresh one line $=(1 \mathrm{~ms})^{*}(0.66)=0.66 \mathrm{~ms}=660$ instructions.
The data must be addressed, retrieved, and shifted. The time for each of these tasks depends on hardware and software design. The fastest addressing scheme is to have the buffer of the picture data on the RAM as shown on Figure 2.

This scheme has the higher byte of the DATA POINTER holding the module line number and the lower byte of the DATA POINTER points to the last byte of the last module (same number for all the lines). Once the DATA POINTER is loaded, a byte of the data is retrieved and shifted, and the lower byte of the DATA POINTER decrements, a byte of the data is retrieved and shifted... and so forth until the lower byte of the DATA POINTER is equal to zero.

Shifting the data with micro-controller pins for many modules is very time consuming. In this example, we assume the data is written via the micro-controller parallel bus to a device such as the 3 -shift register method described above or via serial port ( 8 -bit mode). The shift speed of data must be such that the micro-controller does not wait to write the shift data.

This method takes 3 instructions per byte of data (load the byte, write the byte to the shift register, and decrement the data pointer). If the Interrupt overhead takes 15 instructions, this micro-controller can control 43 modules ( 660 instructions/line - 15 instructions/overhead) / ( 5 bytes/module) * (3 instructions/byte)) $=43$ modules. However, due to driver fan-out and noise the actual number should be less. NKK has controlled 16 to 24 modules using a 74 HC 4050 driver without problems.

## Can the switches be controlled with low voltage?

The specification does not support it, but NKK has tested the switches at 3.0V VDD and signals without problems.

## I only need to display a small picture. Do I need to refresh the whole display?

The FLM signal indicates the first line. If you do not need the display for all the 24 rows of pixels, you can use FLM after as many lines as needed. If you need to center your display, you can toggle the LP to insert a line.

## Is it possible to have an image with shades of gray?

Yes, it is possible. Multiple pictures must be refreshed consecutively. The pixels that are ON for all the pictures will be the darkest, the pixels that are OFF for only one picture will be the second darkest and so on. The number of shades of gray depends on how fast the controller can refresh the LCD.

## How many backlight colors can be achieved?

Infinite. It depends on how many levels of control are present for each discrete color.
For example, with an RGB SmartDisplay and 16 levels of control for each color the total number of colors is $16 \times 16 \times 16=4096$ different colors.

